IN THE CLAIMS:

1. (Currently Amended) An on-screen display apparatus comprising a voltage holding means for holding a voltage value at an input chroma signal DC level at a time when ~~an~~ the input chroma signal is a null signal and is for outputting the voltage value during an on-screen display period.

2. (Currently Amended) An on-screen display apparatus comprising a voltage holding means for holding a voltage value at an input chroma signal DC level at a time when ~~an~~ the input chroma signal is a null signal and means for generating and outputting a chroma signal as a function of the voltage value during an on-screen display period.

3. (Currently Amended) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value at an input chroma signal DC level at a time when ~~an~~ the input chroma signal is a null signal; and

an output switch for outputting a voltage value held by the voltage holding means during an on-screen display period and outputting the input chroma signal at a time other than during the on-screen display period.

4. (Previously Amended) An on-screen display apparatus comprising:

a voltage holding means for holding a voltage value at a time when an input chroma signal is a null signal;

an AC component generation means for generating AC components of the chroma signal;

an adder for adding a voltage value held by the voltage holding means and the AC components of the chroma signal which are generated by the AC component generation means; and

an output switch for outputting a signal added by the adder during an on-screen display period and outputting the input chroma signal at a time other than during the on-screen display period.

5. (Previously Amended) The on-screen display apparatus of Claim 3, wherein the voltage holding means comprises a capacitor for holding a voltage value.

6. (Previously Amended) The on-screen display apparatus of Claim 5, wherein the voltage holding means further comprises a resistor located on a chroma signal input side of the capacitor.

7. (Previously Amended) The on-screen display apparatus of Claim 6, wherein the voltage holding means is located on a chroma signal input side of the resistor, and further comprises a hold timing switch that conducts a input chroma signal when the input chroma signal is a null signal.

8. (Previously Amended) The on-screen display apparatus of Claim 6, wherein the voltage holding means is located between the capacitor and the resistor, and further comprises a hold timing switch that conducts a input chroma signal when the input chroma signal is a null signal.

9. (Previously Amended) The on-screen display apparatus of Claim 3, wherein the voltage holding means comprises:

an AD converter for converting an input chroma signal into a digital signal when the input chroma signal is a null signal;

a storage means for storing a voltage value at the time when the input chroma signal is a null signal, which has been converted into a digital signal by the AD converter; and

a DA converter for converting the voltage value stored in the storage means into an analog signal.

10. (Previously Amended) The on-screen display apparatus of Claim 3, wherein the voltage holding means is for holding the voltage value during a horizontal sync period in which the input chroma signal is a null signal.

11. (Previously Amended) The on-screen display apparatus of Claim 3, wherein the voltage holding means is for holding the voltage value during a vertical sync period in which the input chroma signal is a null signal.